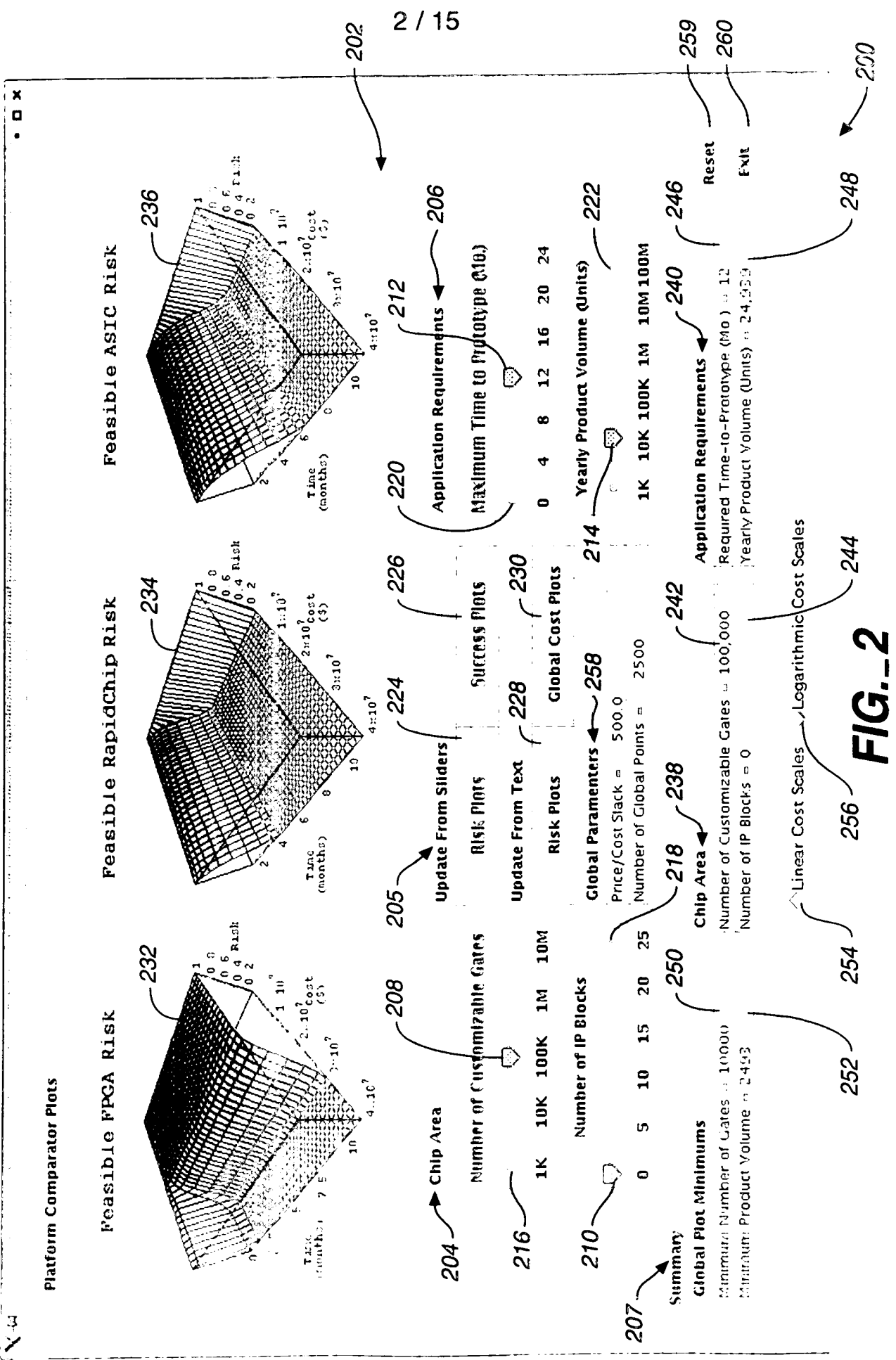
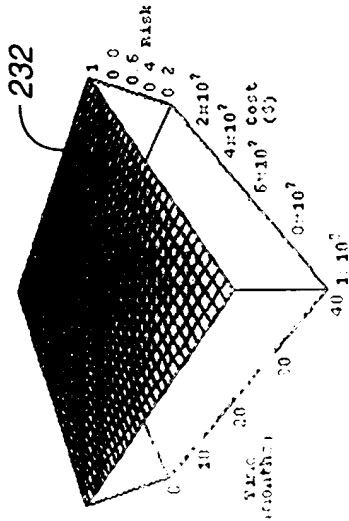


FIG._1

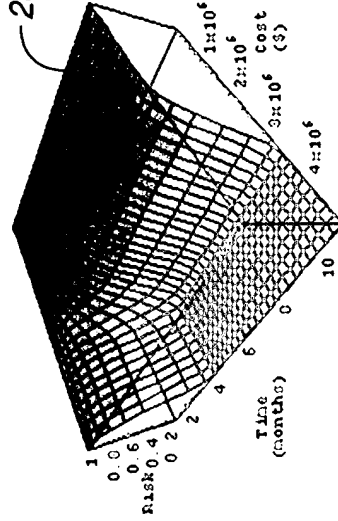


Platform Comparator Plots

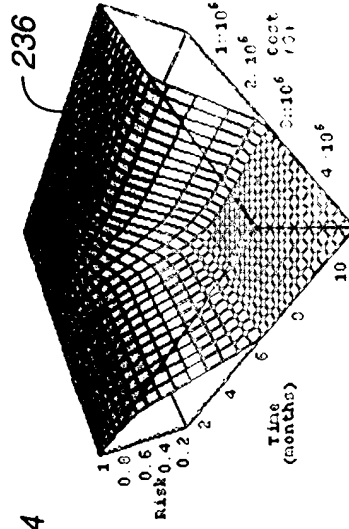
IPDA Infeasible at Given Set Point



Feasible RapidChip Risk



Feasible ASIC Risk



204 → Chip Area

Update From Sliders

Number of Customizable Gates

1K 10K 100K 1M 10M

Number of IP Blocks

0 5 10 15 20 25

Summary

Global Plot Minimums

Minimum Number of Gates = 10000

Minimum Product Volume = 29980

Risk Plots

Update From Text

Risk Plots

Global Cost Plots

Global Parameters

Price/Cost Slack = 500.0

Number of Global Points = 2500

Chip Area

Number of Customizable Gates = 100,000

Number of IP Blocks = 0

Linear Cost Scales Logarithmic Cost Scales

Application Requirements → 206

Maximum Time to Prototype (Mo.)

0 4 8 12 16 20 24

Yearly Product Volume (Units)

1K 10K 100K 1M 10M 100M

Application Requirements → 240

Required Time-to-Prototype (Mo.) = 12

Yearly Product Volume (Units) = 299,800

Reset

Exit

FIG. 3

252

248

200

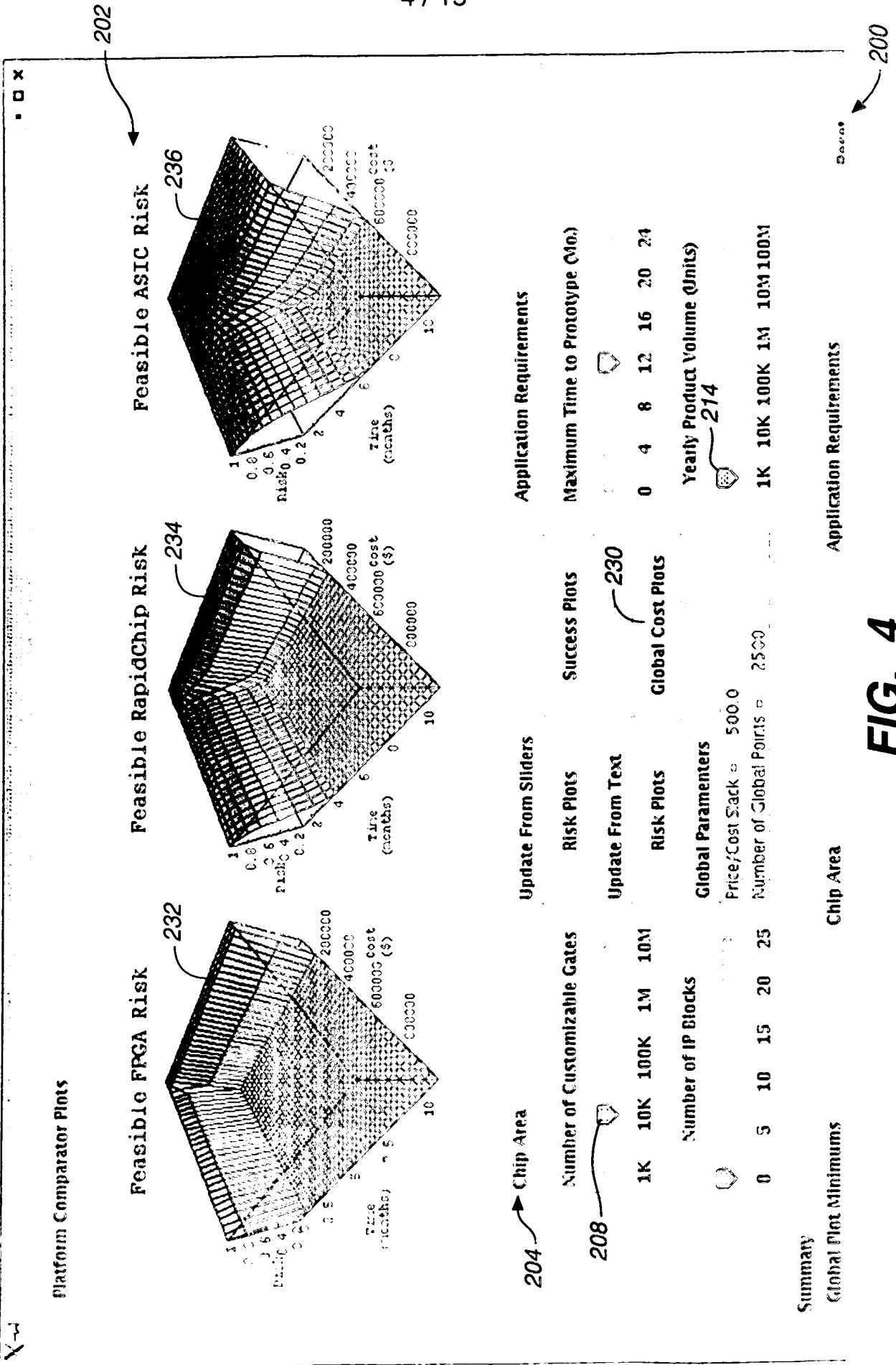


FIG. 4

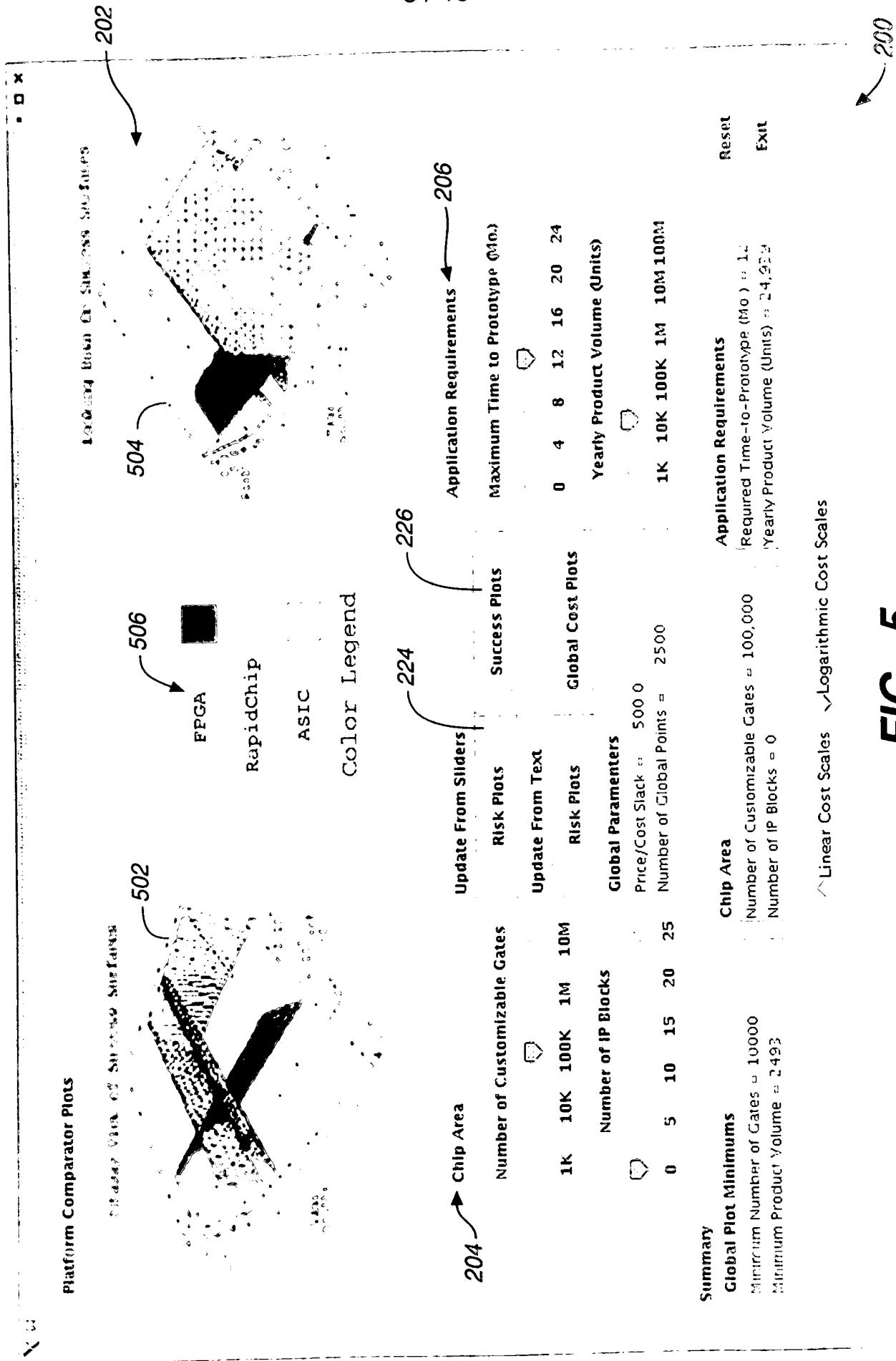
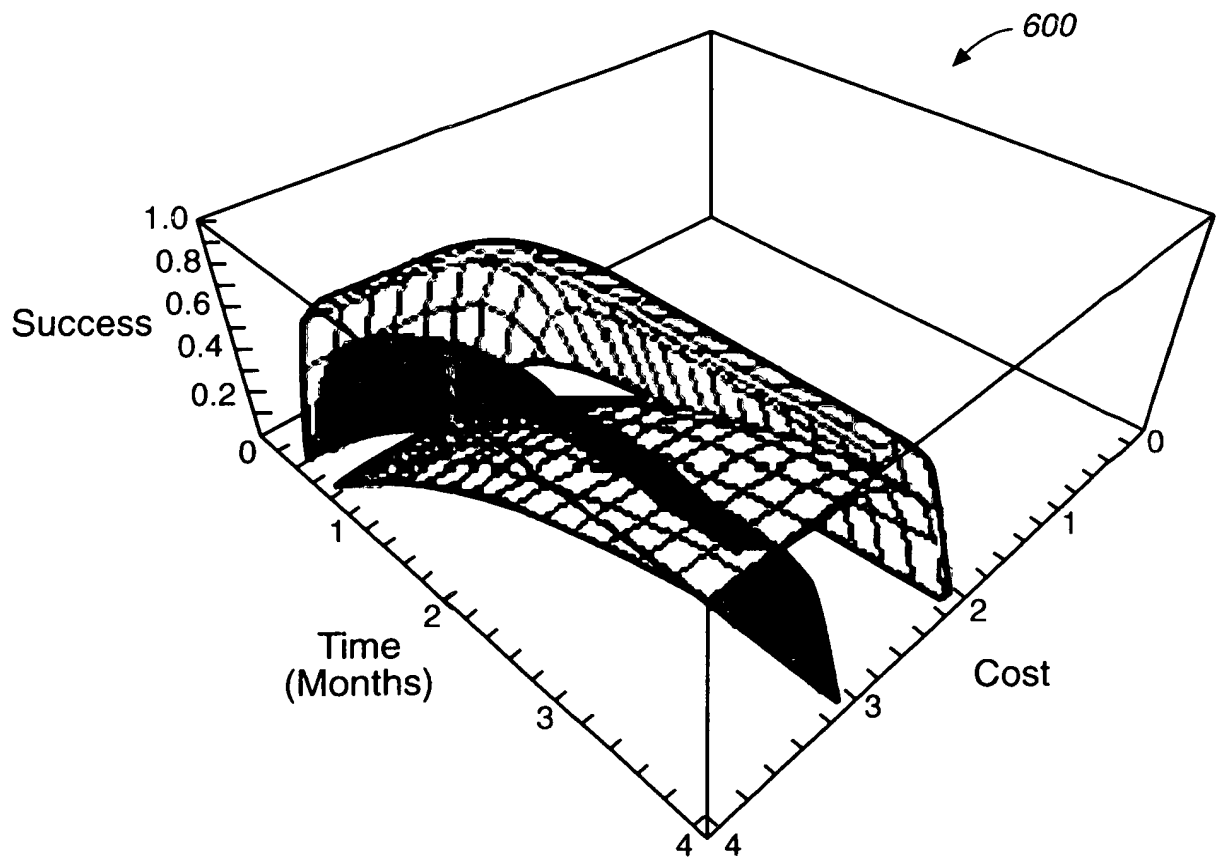


FIG. 5

**FIG._6**

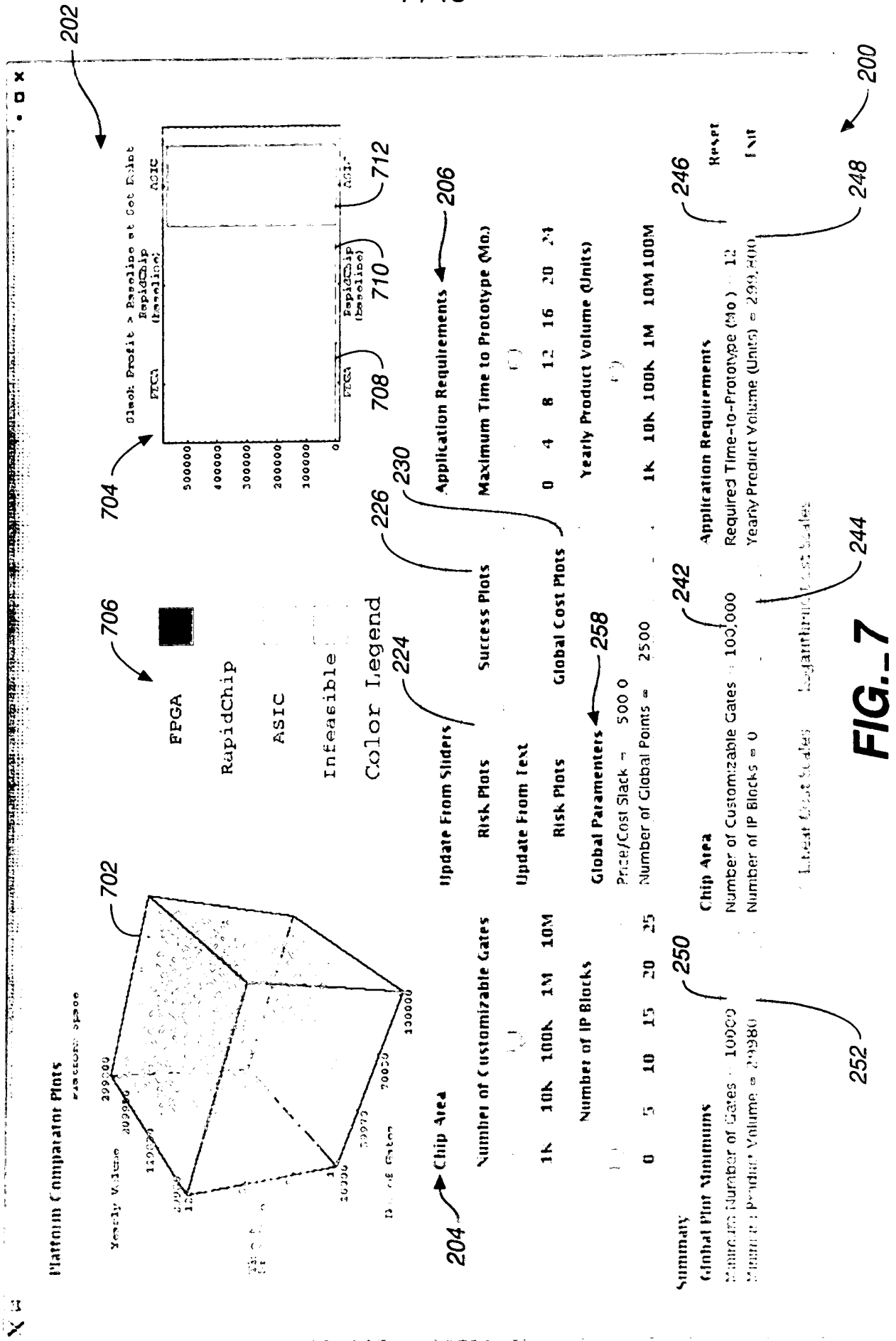
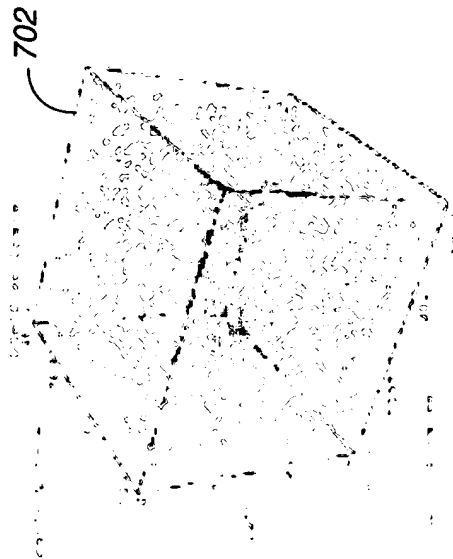


FIG. 7

Platform Comparison Plots



204 → Chip Area

Number of Customizable Gates

1K 10K 100K 1M 10M

Number of IP Blocks

0 5 10 15 20 25

Summary

Global Plot Minimums

Minimum Number of Gates = 1000

Minimum Product Volume = 100

Chip Area

Number of Customizable Gates = 10,000

Number of IP Blocks = 0

Update From Sliders

Number of Customizable Gates

1K 10K 100K 1M 10M

Number of IP Blocks

0 5 10 15 20 25

Risk Plots

Update From Text

Risk Plots

Global Parameters

Price/Cost Slack = 500.0

Number of Global Points = 2500

Success Plots

Global Cost Plots

706

FPGA

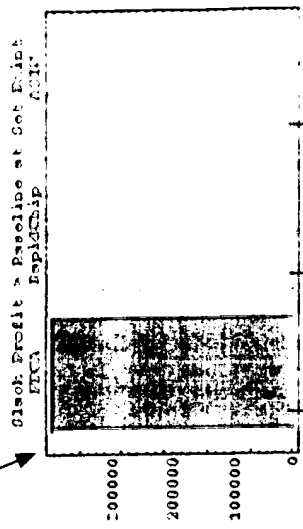
RapidChip

ASIC

Infeasible

Color Legend

704



708

710

712

Application Requirements

Maximum Time to Prototype (Mo)

0 4 8 12 16 20 24

Yearly Product Volume (Units)

1K 10K 100K 1M 10M 100M

Application Requirements

Required Time-to-Prototype (Mo) = 12

Yearly Product Volume (Units) = 1,000

Reset

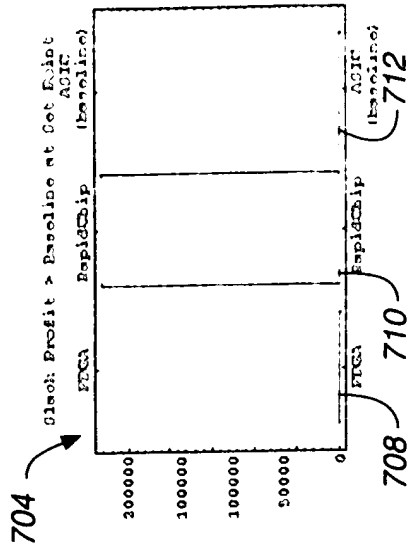
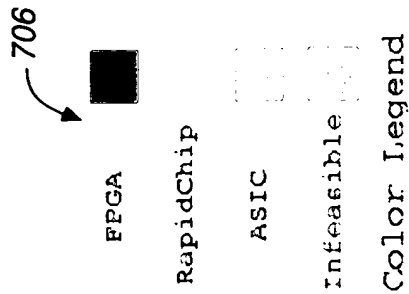
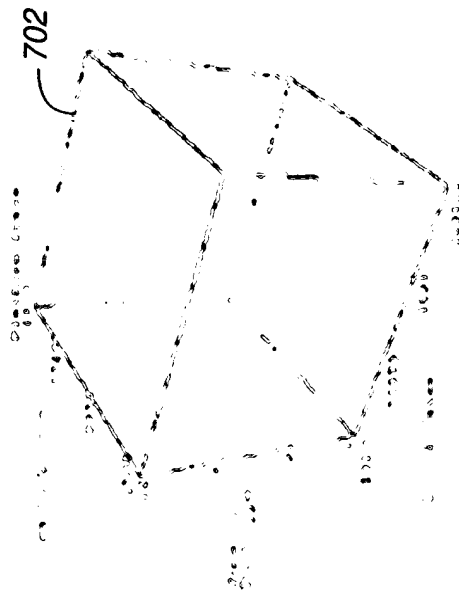
Exit

Linear Cost Scales Logarithmic Cost Scales

FIG._8

200

Platform Comparison Plots



Chip Area

Number of Customizable Gates

1K 10K 100K 1M 10M

Number of IP Blocks

0 5 10 15 20 25

Global Parameters

Price/Cost Slack = 500.0

Number of Global Points = 2500

Update From Sliders

Risk Plots

Update From Text

Risk Plots

Global Cost Plots

Success Plots

Application Requirements

Maximum Time to Prototype (Mo.)

0 4 8 12 16 20 24

Yearly Product Volume (Units)

1K 10K 100K 1M 10M 100M

Summary

Global Plot Minimums

Minimum Number of Gates = 10000

Minimum Product Volume = 2500

Chip Area

Number of Customizable Gates = 100,000

Number of IP Blocks = 0

Application Requirements

Required Time-to-Prototype (Mo.) = 12

Yearly Product Volume (Units) = 25000

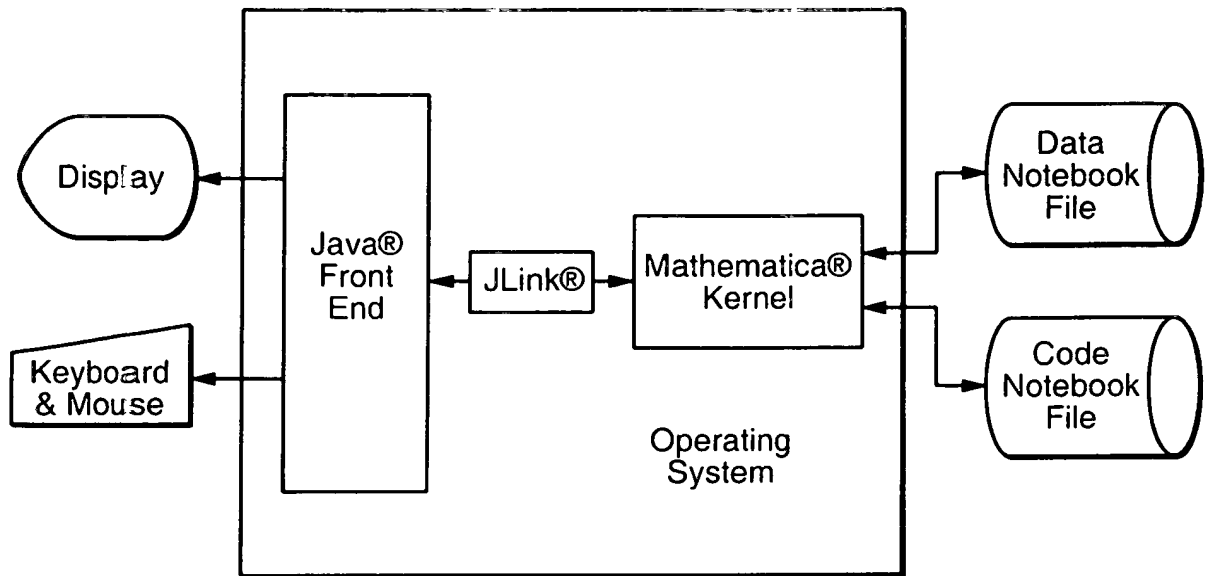
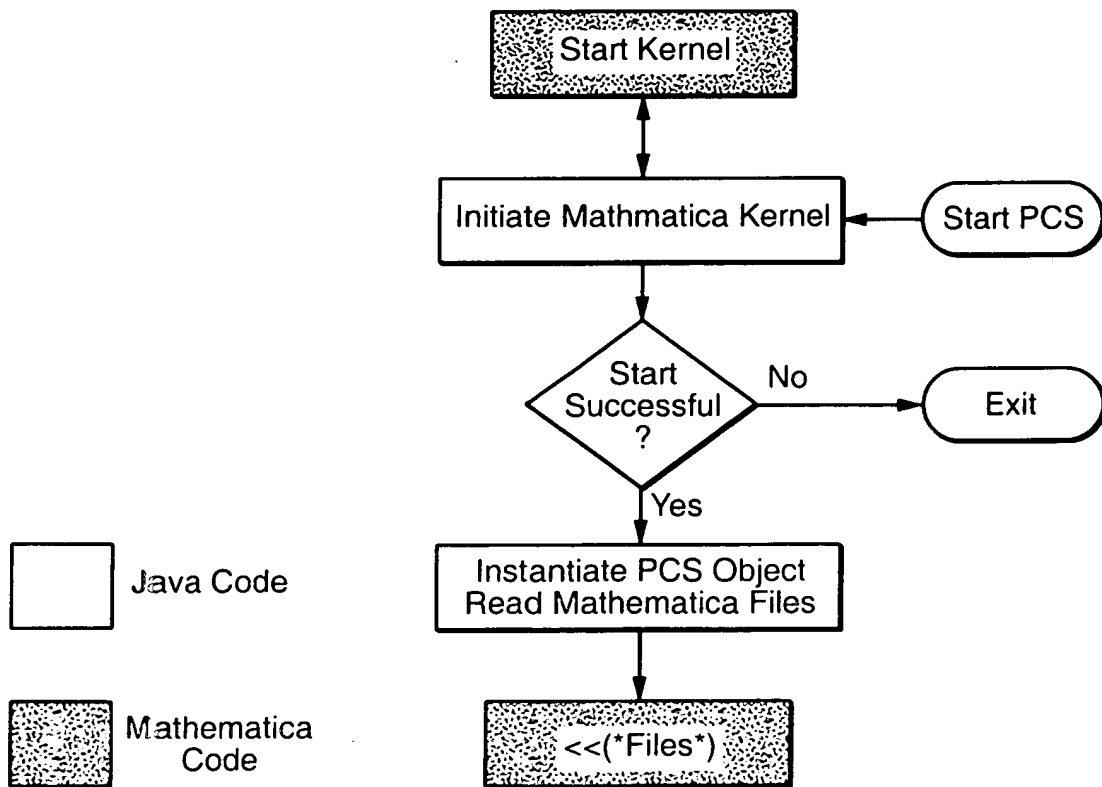
Reset

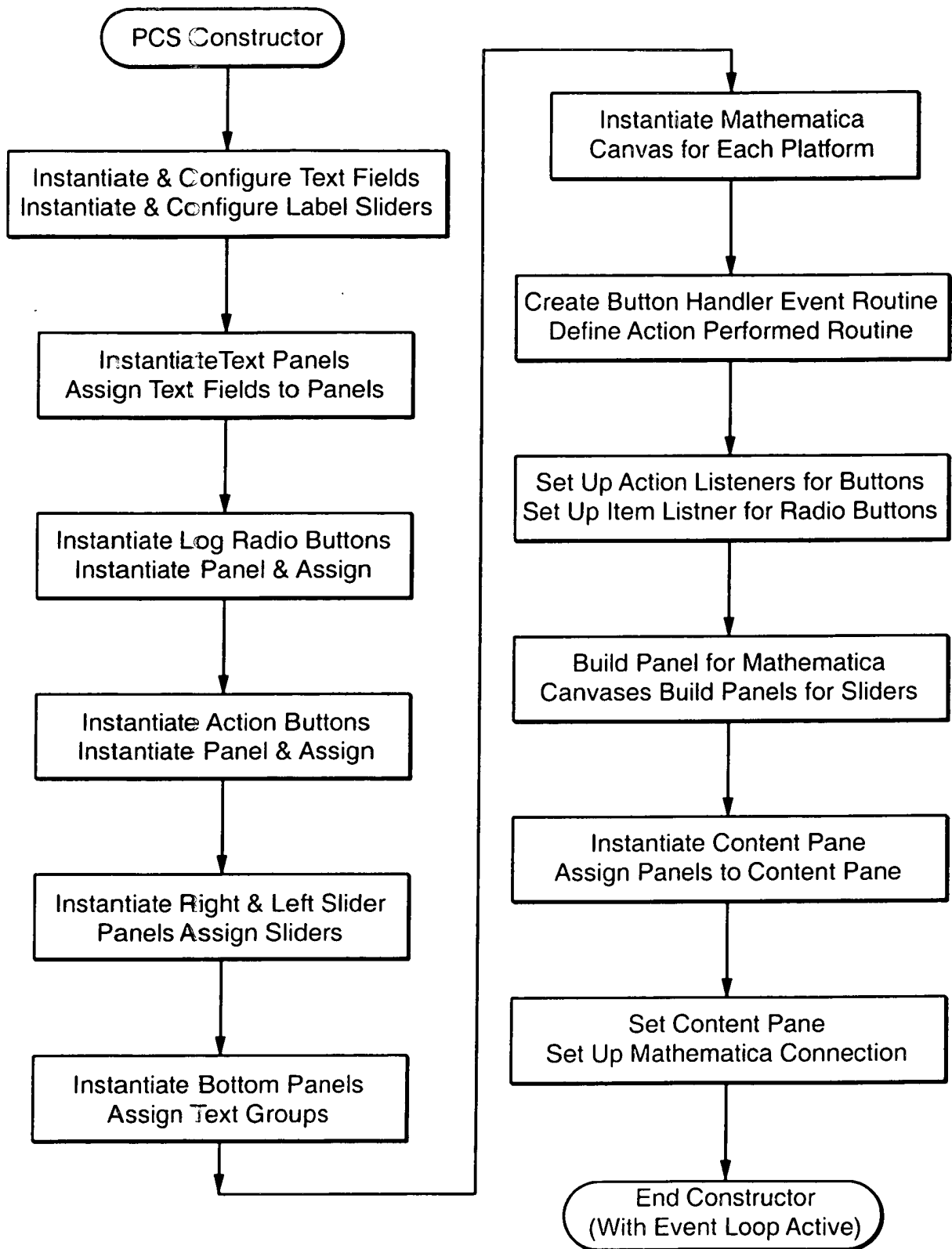
Exit

Linear Cost Scales Logarithmic Cost Scales

FIG.-9

200

**FIG._10****FIG._11**

**FIG._12**

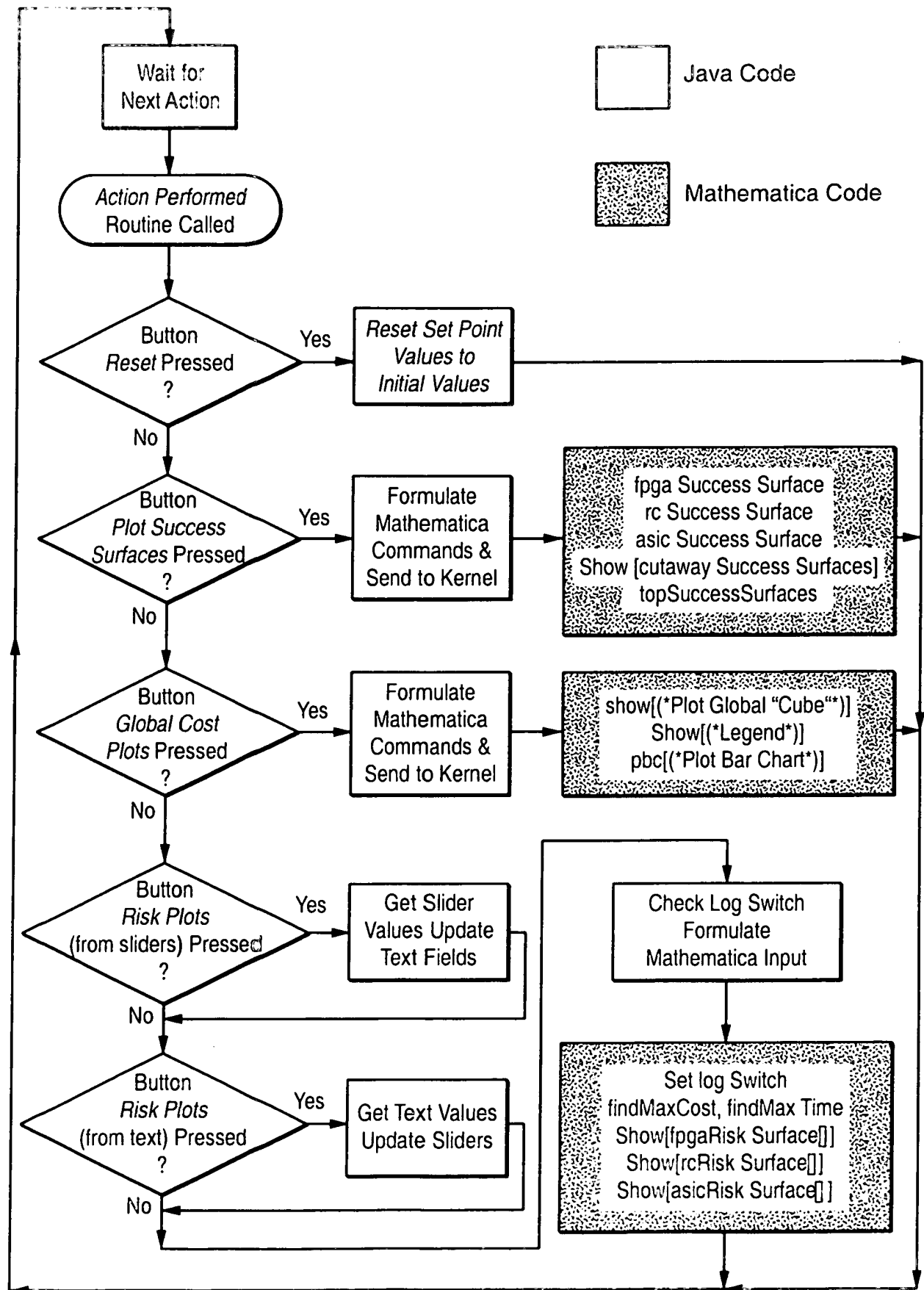
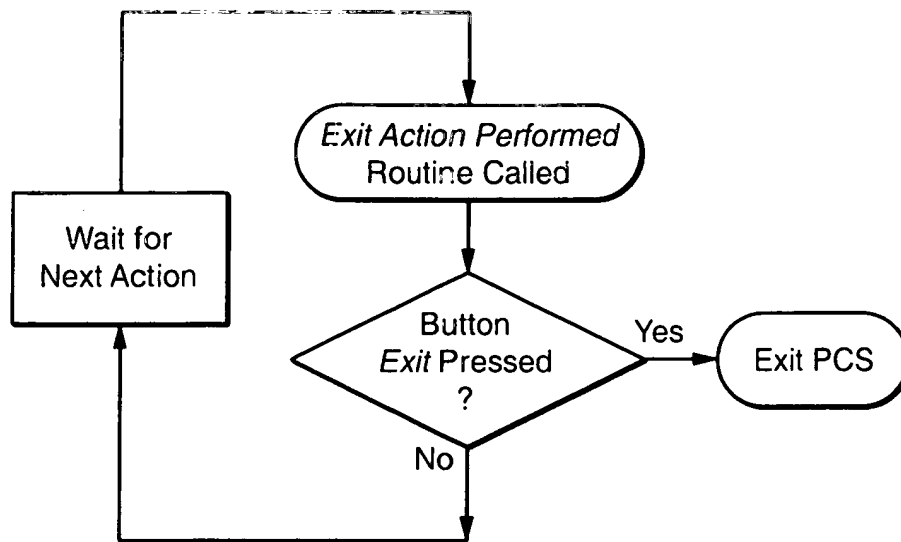
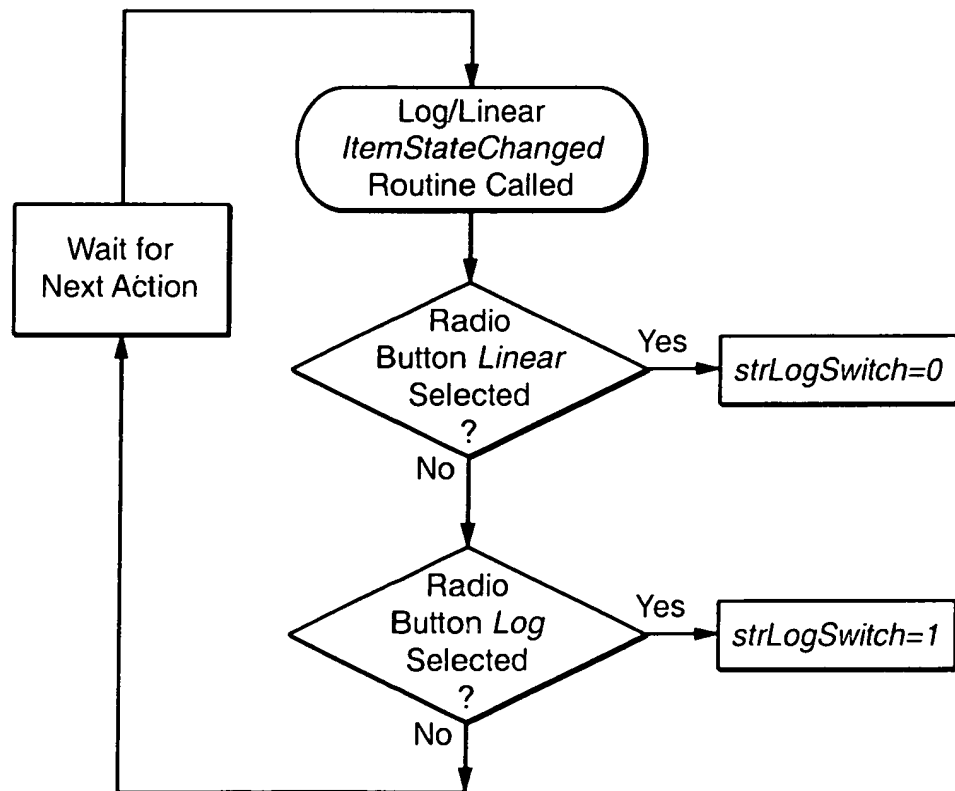
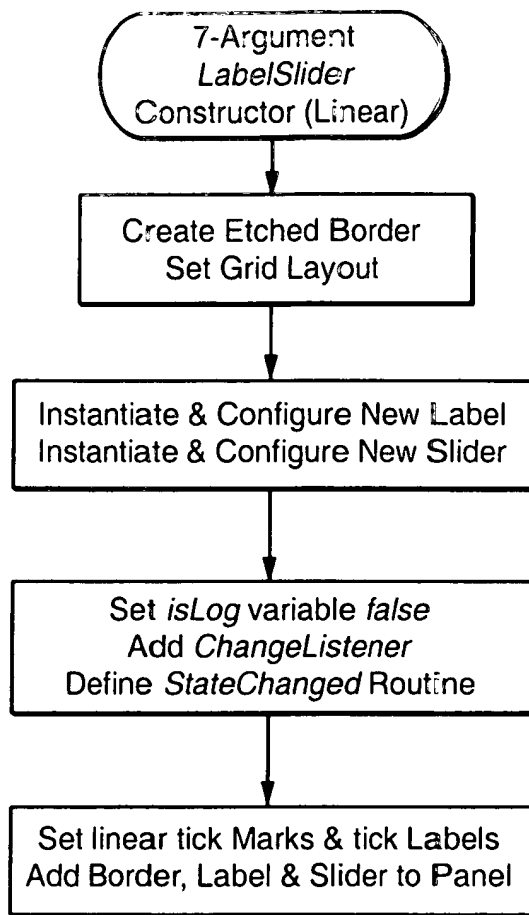
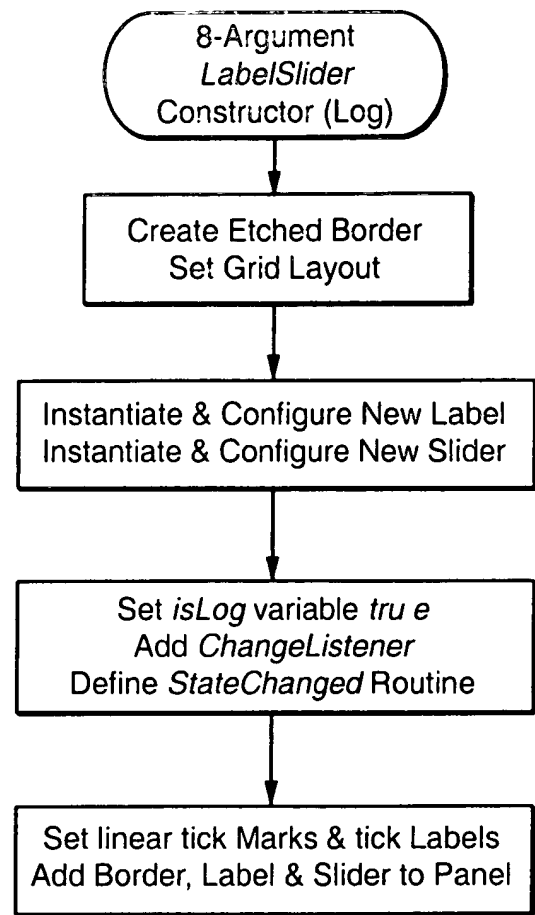
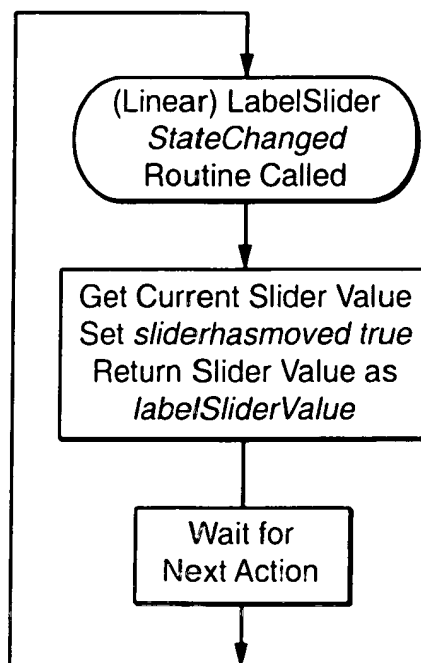
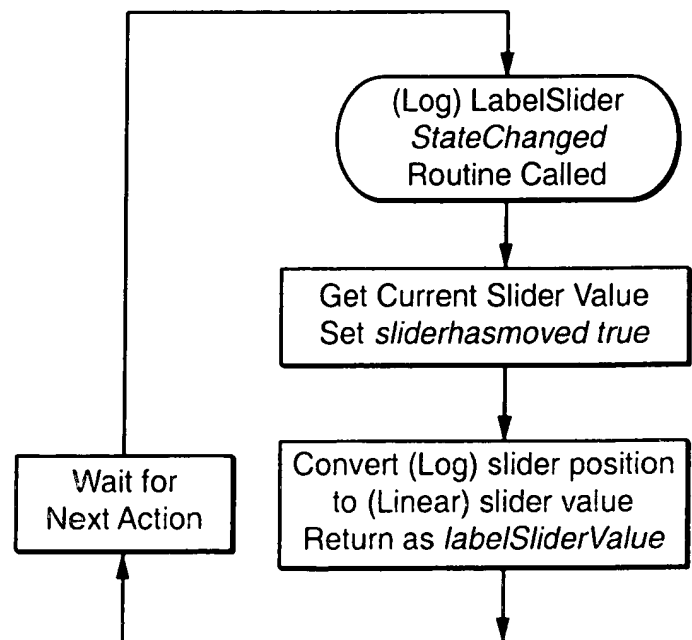
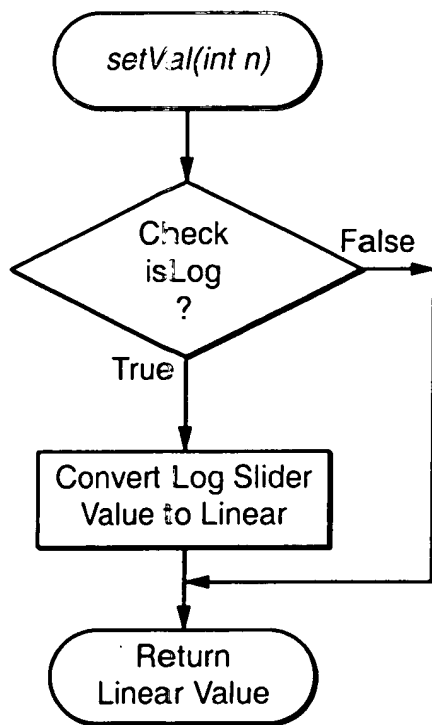
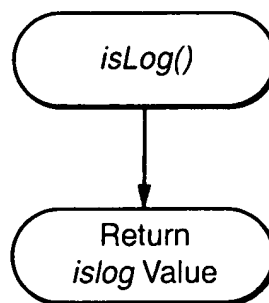
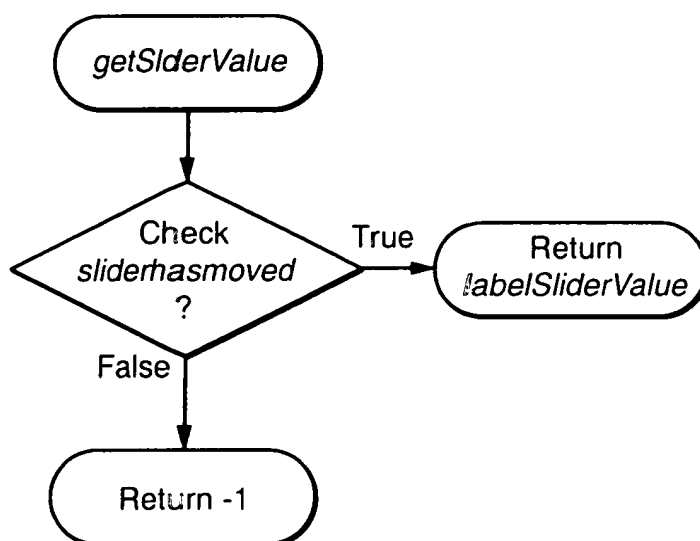


FIG. 13

**FIG. 14****FIG. 15**

**FIG._16****FIG._18****FIG._17****FIG._19**

**FIG._20****FIG._22****FIG._21**

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